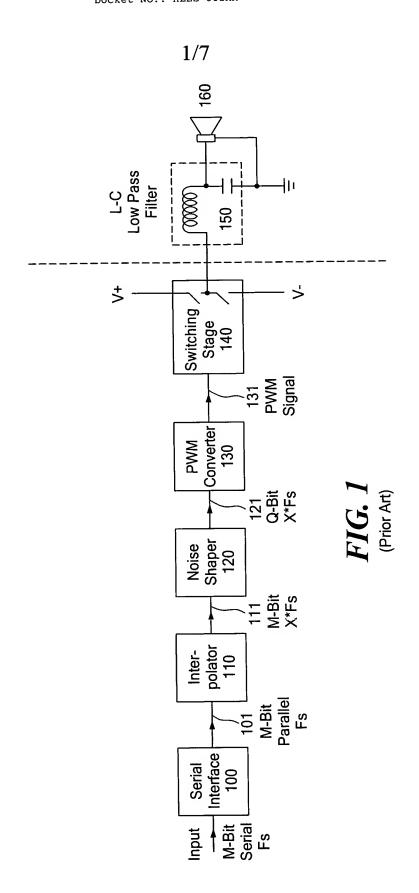
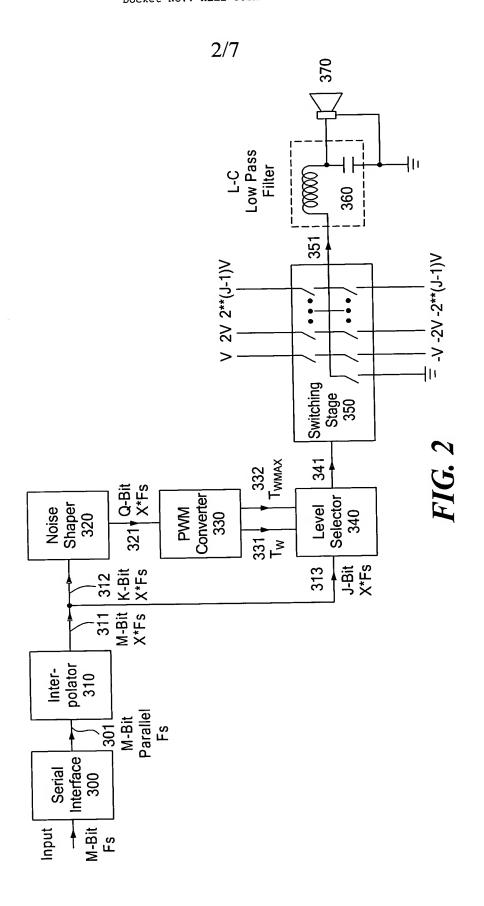
Title: MULTI-LEVEL PULSE WIDTH MODULATION IN DIGITAL SYSTEM Inventor Name: Ying Lau Lee Appl. No. UnAssigned Docket No.: HLEE-001XX



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Appl. No. UnAssigned Docket No.: HLEE-001XX

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- =- Input Data Sample

Ts Sampling Cycle-Time

Tw PWM Pulse Width

TWMAX PWM Pulse Maximum Width

-B- Base Level

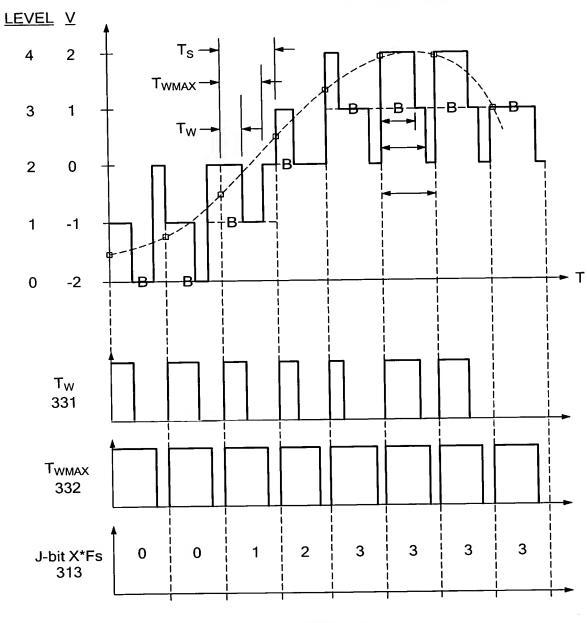
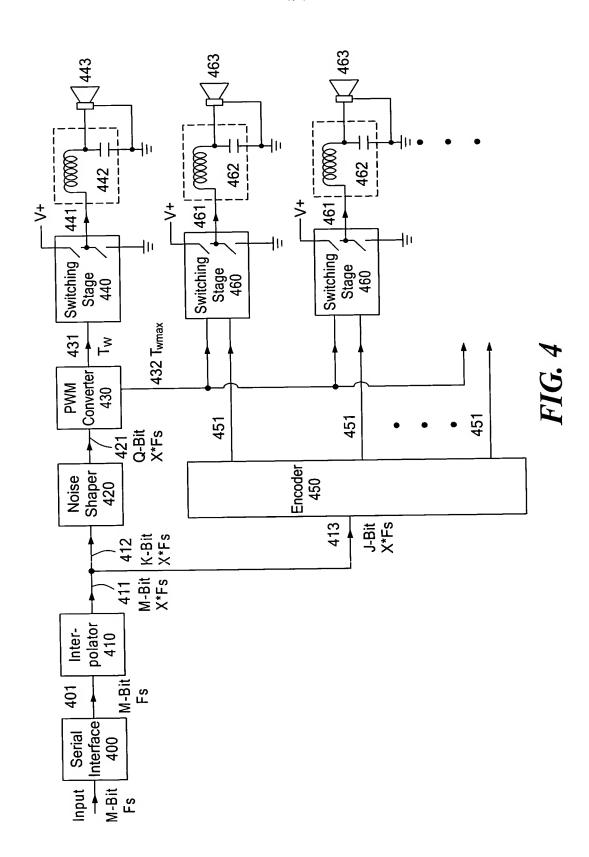


FIG. 3

Title: MULTI-LEVEL PULSE WIDTH MODULATION IN DIGITAL SYSTEM Inventor Name: Ying Lau Lee

Appl. No. UnAssigned Docket No.: HLEE-001XX

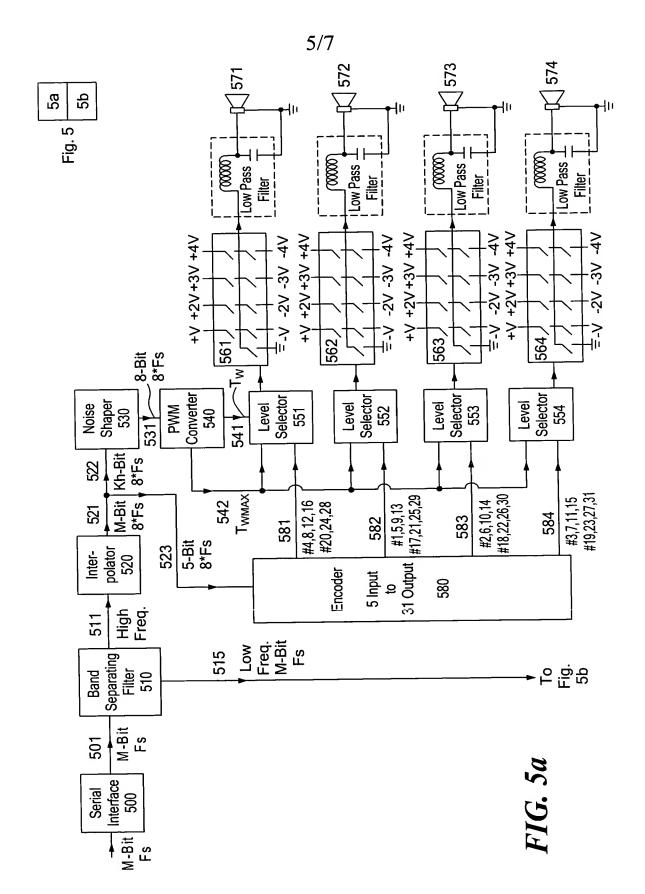
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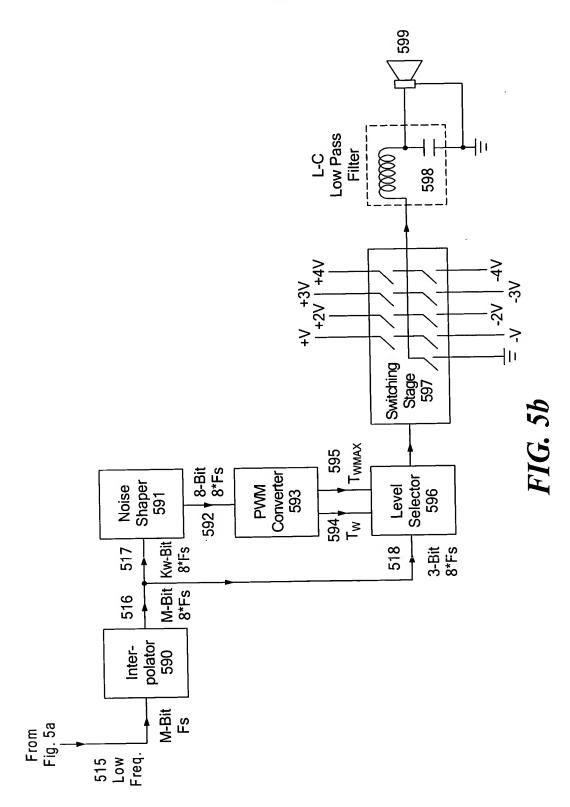
Title: MULTI-LEVEL PULSE WIDTH MODULATION IN DIGITAL SYSTEM Inventor Name: Ying Lau Lee Appl. No. UnAssigned

Appl. No. UnAssigned Docket No.: HLEE-001XX



Title: MULTI-LEVEL PULSE WIDTH MODULATION IN DIGITAL SYSTEM Inventor Name: Ying Lau Lee Appl. No. UnAssigned Docket No.: HLEE-001XX

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Title: MULTI-LEVEL PULSE WIDTH MODULATION IN DIGITAL SYSTEM Inventor Name: Ying Lau Lee Appl. No. UnAssigned Docket No.: HLEE-001XX

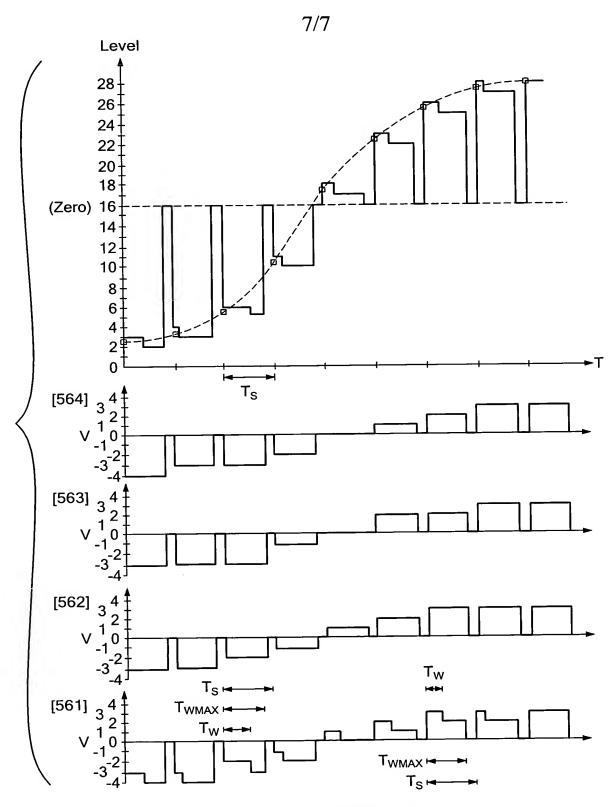


FIG. 6